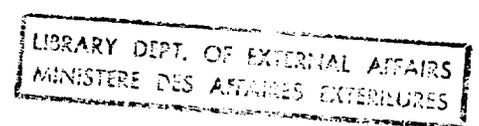


TABLE OF CONTENTS

Introduction	2
The digital revolution	3
Canada's digital switchover	4
Northern Telecom's OPEN World	7
Fibre optics	9
Computer communications	12
Telidon	15
Databases	18
iNET 2000	19
Space	20
Broadcasting	21
Cellular mobile radio	23
Glossary	24

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Cover:
 Processed silicon wafers each
 containing hundreds of chips.

Inside front cover:
 Microphotograph of a section of a chip
 displaying pathways of the circuits.

The purpose of the science and technology series is to inform readers of current trends in Canadian research and development. Only Canadian designs are discussed at length in this series, rather than designs of other countries which may be developed and produced in Canada.

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