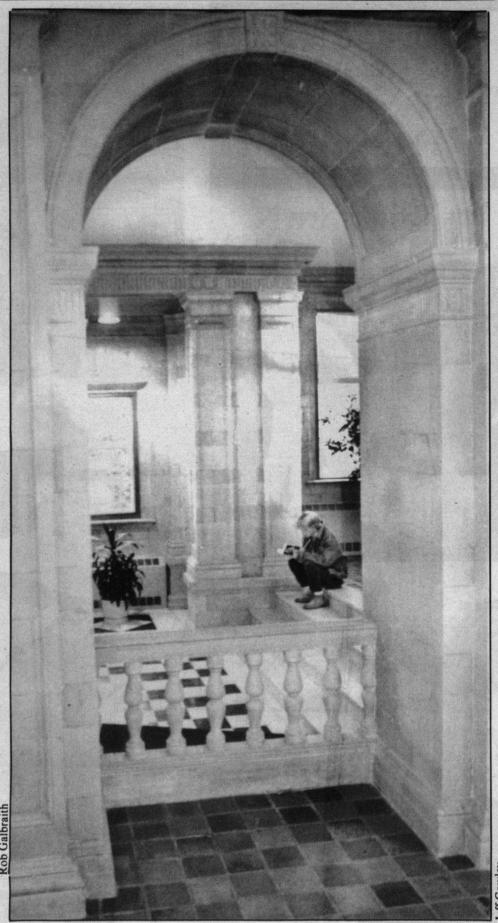


2:20 pm — Ray Cypher and Minh Le (both third year Electrical Engineering students) graph Diode Response curves in a lab on the third floor of the EE building. They are enrolled in EE470, Semiconductor and transistor technology, a mandatory course for all Electrical and Computer engineers.



3:15 pm — Thirsty students watch the floor numbers in the elevator to RATT.



5:00 pm — Michael Francis (3rd year Arts) at work in the lobby of the recently renovated Old Arts Building.

3:20 pm — Chemistry E1-60 on exam day. The bodies have been removed but the screams of anguish still resonate.

